



Docket No.: M4065.0112/P112-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Application No.: 09/686,943

Group Art Unit: 2823

Filed: October 12, 2000

Examiner: K. Nguyen

For: MICROLENS ARRAY WITH IMPROVED
FILL FACTOR

#7/C
Amend
NE

JM Ginnell
4/17/03

TECHNOLOGY CENTER 2800

APR 15 2003

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5/13/03
V. Hunt

AMENDMENT UNDER 37 CFR § 1.116

Box AF
Commissioner for Patents
Washington, DC 20231

Please enter
the amendment
K.N.
05/16/03

Dear Sir:

In response to the Office Action dated January 14, 2003 (Paper No. 6), finally rejecting claims 60-83, 85-87, 89, 90, and 94-99, please amend the above-identified U.S. patent application as follows:

In the Written Description

Please rewrite the paragraphs on page 9, lines 4-23, as follows:

C1
The microlens array 22 is manufactured through a process described as follows, and illustrated by Figs. 5 through 8. Referring now to Fig. 5, a substrate 30, which may be any of the types of substrates described above, having a pixel array 26, peripheral circuits, contacts and wiring formed thereon by well-known methods, is provided. A protective layer 24 of BPSG, BSG, PSG, silicon dioxide, silicon nitride or the like is formed over the pixel array 26 to passivate it and to provide a planarized surface. A spacing layer 25 is formed over the protective layer 24. A lens forming layer 80 is formed on the spacer layer